

What Is Claimed Is:

1. A color filter substrate for a liquid crystal display device, comprising:
  - a substrate;
  - a plurality of black matrix regions on the substrate;
  - a plurality of color filter layers, each disposed between adjacent ones of the
  - plurality of black matrix regions;
  - a panel identification at an edge of the substrate spaced apart from the
  - plurality of black matrix regions, the panel identification and the plurality of color
  - filter layers including a same material;
  - an overcoat layer on the plurality of color filter layers; and
  - a common electrode on the overcoat layer.
2. The color filter substrate according to claim 1, wherein an uppermost surface of each of the plurality of black matrix regions is at least as high as an uppermost surface of each of the plurality of color filter layers from a surface of the substrate.

3. The color filter substrate according to claim 1, wherein an uppermost surface of each of the plurality of color filter layers is at least as high as an uppermost surface of each of the plurality of black matrix regions from a surface of the substrate.

4. The color filter substrate according to claim 1, wherein an uppermost surface of each of the plurality of black matrix regions is at least as high as an uppermost surface of the panel identification from a surface of the substrate.

5. The color filter substrate according to claim 1, wherein an uppermost surface of each of the plurality of black matrix regions, an uppermost surface of each of the plurality of color filter layers, and an uppermost surface of the panel identification are all coplanar.

6. A color filter substrate for a liquid crystal display device, comprising:  
a substrate;  
a plurality of black matrix regions on the substrate;  
a panel identification at an edge of the substrate spaced apart from the plurality of black matrix regions, the panel identification and the black matrix include a same material;

a plurality of color filter layers on the substrate;  
an overcoat layer on the plurality of color filter layers; and  
a common electrode on the overcoat layer,  
wherein a thickness of the plurality of black matrix regions is equal to a  
thickness of the plurality of color filter layers.

7. The color filter substrate according to claim 6, wherein an uppermost surface  
of each of the plurality of black matrix regions is at least as high as an uppermost  
surface of the panel identification from a surface of the substrate.

8. A manufacturing method of a color filter substrate for a liquid crystal display  
device, comprising:

forming a plurality of black matrix regions on a substrate;  
forming a plurality of color filter layers, each disposed between adjacent  
ones of the plurality of black matrix regions;  
forming a panel identification at an edge of the substrate spaced apart  
from the plurality of black matrix regions;  
forming an overcoat layer on the plurality of color filter layers; and  
forming a common electrode on the overcoat layer,

wherein the panel identification and the plurality of color filter layers are formed from a same material.

9. The method according to claim 8, wherein the step of forming a plurality of color filter layers includes forming a photosensitive film on the plurality of black matrix regions.

10. The method according to claim 9, wherein the step of forming a plurality of color filter layers includes irradiating laser light onto the photosensitive film.

11. The method according to claim 8, wherein the step of forming a plurality of color filter layers includes an ink jet process.

12. The method according to claim 8, wherein the step of forming a plurality of color filter layers and the step of forming a panel identification are performed simultaneously.

13. The method according to claim 8, wherein an uppermost surface of each of the plurality of black matrix regions is at least as high as an uppermost surface of each of the plurality of color filter layers from a surface of the substrate.

14. The method according to claim 8, wherein an uppermost surface of each of the plurality of color filter layers is at least as high as an uppermost surface of each of the plurality of black matrix regions from a surface of the substrate.

15. The color filter substrate according to claim 8, wherein an uppermost surface of each of the plurality of black matrix regions is at least as high as an uppermost surface of the panel identification from a surface of the substrate.

16. A manufacturing method of a color filter substrate for a liquid crystal display device, comprising:

forming a plurality of black matrix regions on a substrate;

forming a plurality of color filter layers of a first material, each disposed between adjacent ones of the plurality of black matrix regions; and

forming a panel identification of the first material on the substrate spaced apart from the plurality of black matrix regions.

17. The method according to claim 16, wherein the step of forming a plurality of color filter layers includes forming a photosensitive film on the plurality of black matrix regions.

18. The method according to claim 17, wherein the step of forming a plurality of color filter layers includes irradiating laser light onto the photosensitive film.

19. The method according to claim 16, wherein the step of forming a plurality of color filter layers and the step of forming a panel identification are performed simultaneously.

20. The method according to claim 16, wherein an uppermost surface of each of the plurality of black matrix regions is at least as high as an uppermost surface of each of the plurality of color filter layers from a surface of the substrate.

21. The method according to claim 16, wherein an uppermost surface of each of the plurality of color filter layers is at least as high as an uppermost surface of each of the plurality of black matrix regions from a surface of the substrate.

22. The color filter substrate according to claim 16, wherein an uppermost surface of each of the plurality of black matrix regions is at least as high as an uppermost surface of the panel identification from a surface of the substrate.